

DECLARATION
and POWER OF ATTORNEY

☒ ORIGINAL
☐ CONTINUATION
☐ DIVISIONAL

As a below named inventor, I declare that the information given herein is true, that I believe that I am the original, first and sole inventor if only one name is listed at 1 below, or a joint inventor if plural inventors are named below at 1-4, of the invention entitled:

SEMICONDUCTOR INPUT PROTECTION CIRCUIT

Which is described and claimed in:

- ☒ the attached specification or
☐ the specification in application Serial No. _____
☐ as amended on _____

and for which a patent is sought, and that my residence, post office address and citizenship are as stated below next to my name.

I acknowledge my duty to disclose information which is material to the examination of this application in accordance with Title 37, Code of Federal Regulations §1.56(a).

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

I hereby claim foreign priority benefits under Title 35, United States Code, §119 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

PRIOR FOREIGN APPLICATION(S)

| COUNTRY | APPLICATION NUMBER | DATE OF FILING Month/Day/Year | PRIORITY CLAIMED UNDER 35 U.S.C. 119 |
|---------|--------------------|----------------------------------|---|
| JAPAN | 2000-317557 | 10/18/2000 | Yes <input type="checkbox"/> No <input checked="" type="checkbox"/> |

I hereby claim the benefit under title 35, United States Code, Section 119(e) of any United States provisional application(s) listed below

APPLICATION NO. _____ Date _____

I hereby claim the benefit under Title 35, United States Code, Section 120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, Section 112, I acknowledge the duty to disclose all information known to me to be material to patentability as defined in Title 37, Code of Federal Regulations, Section 1.56 which became available between the filing date of the prior application and the national or PCT international filing date of this application:

APPLICATION NO. _____ Filing Date _____ Status - (patented, pending, abandoned) _____

I hereby appoint

Roger R. Wise, Reg. No. 31,204; Paul N. Kokulis, Reg. No. 16,773; Raymond F. Lippitt, Reg. No. 17,519; G. Lloyd Knight, Reg. No. 17,698; George M. Sirilla, Reg. No. 18,221; Kevin E. Joyce, Reg. No. 20,508; Jay M. Finkelstein, Reg. No. 21,082; G. Paul Edgell, Reg. No. 24,238; Donald J. Bird, Reg. No. 25,323; Peter W. Gowdey, Reg. No. 25,872; Richard H. Zaitlen, Reg. No. 27,248; Glenn J. Perry, Reg. No. 28,458; Dale S. Lazar, Reg. No. 28,872; Kendrew H. Colton, Reg. No. 30,368; Mark G. Paulson, Reg. No. 30,793; Ruth N. Morduch, Reg. No. 31,044; Paul E. White, Jr., Reg. No. 31,097; Stephen C. Glazier, Reg. No. 31,361; Paul G. Nagy, Reg. No. 37,896; Steven W. Smyrski, Reg. No. 38,312; Eric S. Chen, Reg. No. 43,542; Vivian S. Shin, Reg. No. 43,919 my patent attorneys of PILLSBURY MADISON & SUTRO LLP.

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| | | |

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

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| <i>Nobuaki Tsuji</i> SIGNATURE OF INVENTOR 1 | <i>Masao Noro</i> SIGNATURE OF INVENTOR 2 |
| DATE <i>Oct. 1, 2001</i> | DATE <i>Oct. 2, 2001</i> |
| <i>Terumitsu Maeno</i> SIGNATURE OF INVENTOR 3 | <i>Seiji Hirade</i> SIGNATURE OF INVENTOR 4 |
| DATE <i>Oct. 1, 2001</i> | DATE <i>Oct. 3, 2001</i> |